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CONFIRMATION NO. 7696

SERIAL NUMBER 10/650,692	FILING DATE 08/29/2003 RULE	CLASS 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. 020330A
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APPLICANTS

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** CONTINUING DATA *****

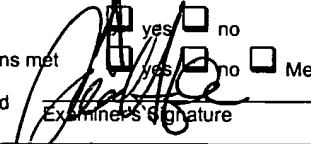

This application is a DIV of 10/100,011 03/19/2002 PAT 6,875,638

** FOREIGN APPLICATIONS *****

JAPAN 2001-335413 10/31/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/09/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 15	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature:  Initials: 				

ADDRESS

38834

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TITLE

Manufacturing method of a semiconductor device incorporating a passive element and a redistribution board

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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